

# Product Specification

HANNTU LCD

(V) Preliminary Specification

( ) Final Specification

|                   |                      |
|-------------------|----------------------|
| <b>Module</b>     | 23.8" TFT-LCD Module |
| <b>Model Name</b> | H238HDL              |

|   |             |
|---|-------------|
| <b>Customer</b>   | <b>Date</b> |
| _____   | _____       |
| <b>Approved by</b>  |             |
| _____   | _____       |
| Note: This Specification is subject to change without notice. |             |

|                                  |             |
|----------------------------------|-------------|
| <b>Checked &amp; Approved by</b> | <b>Date</b> |
|                                  | 2018/07/09  |
| <b>Prepared by</b>               | <b>Date</b> |
|                                  | 2018/07/09  |
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## 1. Handling Precautions

- 1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) To avoid ESD (Electro Static Discharge) damage, be sure to ground yourself before handling TFT-LCD Module.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
- 12) Small amount of materials without flammability grade are used in the TFT-LCD module. The TFT-LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time and lamp ignition voltage.
- 14) Continuous operating TFT-LCD display under low temperature environment may accelerate lamp exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.

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## 2. General Description

HT238HDL-500 is a Color Active Matrix Liquid Crystal Display composed of a TFT-LCD panel, a driver circuit, and a backlight system. The screen format is intended to support the (1920(H) x 270(V)) screen and 16.7M colors (RGB 6-bits + HiFRC data). All input signals are 2-channel LVDS interface.

### 2.1 Display Characteristics

The following items are characteristics summary on the table under 25 °C condition:

| Items   | Unit                 | Specifications                      |
|---|----------------------|-------------------------------------|
| Screen Diagonal   | [mm]                 | 604.5(23.8")                        |
| Active Area   | [mm]                 | 597.6(H) x 84.04(V)                 |
| Pixels H x V  |                      | 1920(x3) x 270                      |
| Pixel Pitch   | [mm]                 | 0.31125per one triad) x 0.31125     |
| Pixel Arrangement   |                      | R.G.B. Vertical Stripe              |
| Display Mode  |                      | Normally White                      |
| White Luminance   | [cd/m <sup>2</sup> ] | 500(center, Typ)                    |
| Contrast Ratio  |                      | 1000 : 1 (Typ)                      |
| Optical Response Time                                     | [msec]               | 10 ms(Typ, on/off)                  |
| Nominal Input Voltage VDD                                 | [Volt]               | +5.0 V                              |
| Power Consumption   | [Watt]               | TBD (Typ)                           |
| Weight  | [Grams]              | TBD (Typ)                           |
| Physical Size (H x V x D)                                 | [mm]                 | 627.6(H) x 114.04(V) x 20 (D) (Typ) |
| Electrical Interface                                      |                      | Dual channel LVDS                   |
| Surface Treatment   |                      | Anti-glare, Hardness 3H             |
| Support Color   |                      | 16.7M colors (RGB 6-bit + Hi_FRC)   |
| Temperature Range<br>Operating<br>Storage (Non-Operating) | [°C]<br>[°C]         | 0 to +50<br>-20 to +70              |
| RoHS Compliance   |                      | RoHS Compliance                     |

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## 2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature).

| Item                                   | Unit                 | Conditions                           | Min.     | Typ.     | Max.  | Note |
|--|----------------------|--------------------------------------|----------|----------|-------|------|
| Viewing Angle                          | [degree]             | Horizontal (Right)<br>CR = 10 (Left) | 89<br>89 | 89<br>89 | -     | 1    |
|  |                      | Vertical (Up)<br>CR = 10 (Down)      | 89<br>89 | 89<br>89 | -     |      |
| Contrast Ratio                         |                      | Normal Direction                     | 600      | 1000     | -     | 2    |
| Central Luminance                      | [cd/m <sup>2</sup> ] |                                      | 280      | 500      | -     | 2    |
| Optical Response Time                  | [msec]               | Raising Time (TrR)                   | -        | TBD      | TBD   | 3    |
|  |                      | Falling Time (TrF)                   | -        | TBD      | TBD   |      |
|  |                      | Rising + Falling                     | -        | 10       | TBD   |      |
| Color / Chromaticity Coordinates (CIE) |                      | Red x                                | TBD      | TBD      | TBD   | 2    |
|  |                      | Red y                                | TBD      | TBD      | TBD   |      |
|  |                      | Green x                              | TBD      | TBD      | TBD   |      |
|  |                      | Green y                              | TBD      | TBD      | TBD   |      |
|  |                      | Blue x                               | TBD      | TBD      | TBD   |      |
|  |                      | Blue y                               | TBD      | TBD      | TBD   |      |
|  |                      | White x                              | 0.263    | 0.313    | 0.363 |      |
|  |                      | White y                              | 0.279    | 0.329    | 0.379 |      |
| Luminance Uniformity                   | [%]                  | 9 Points                             | 75       | 80       | -     | 4,5  |
| NTSC                                   | [%]                  |                                      | -        | 70       | -     |      |

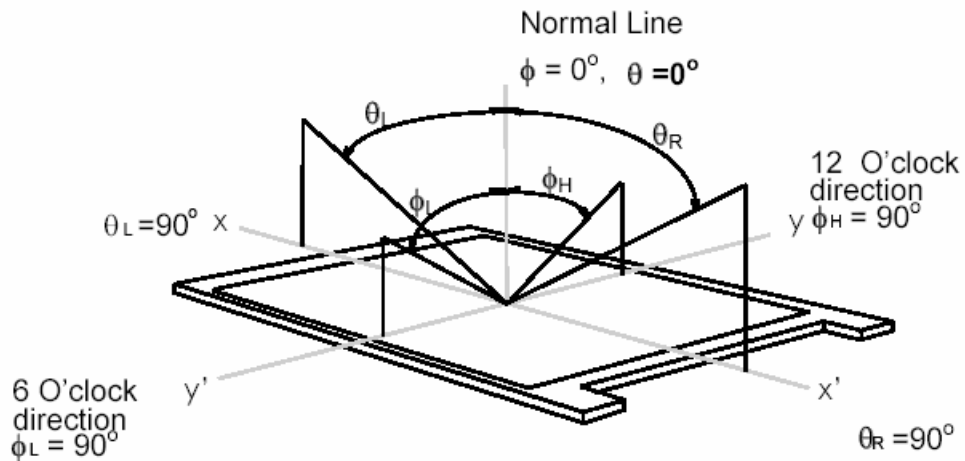
Optical Equipment: BM-5A, BM-7, PR880, or equivalent

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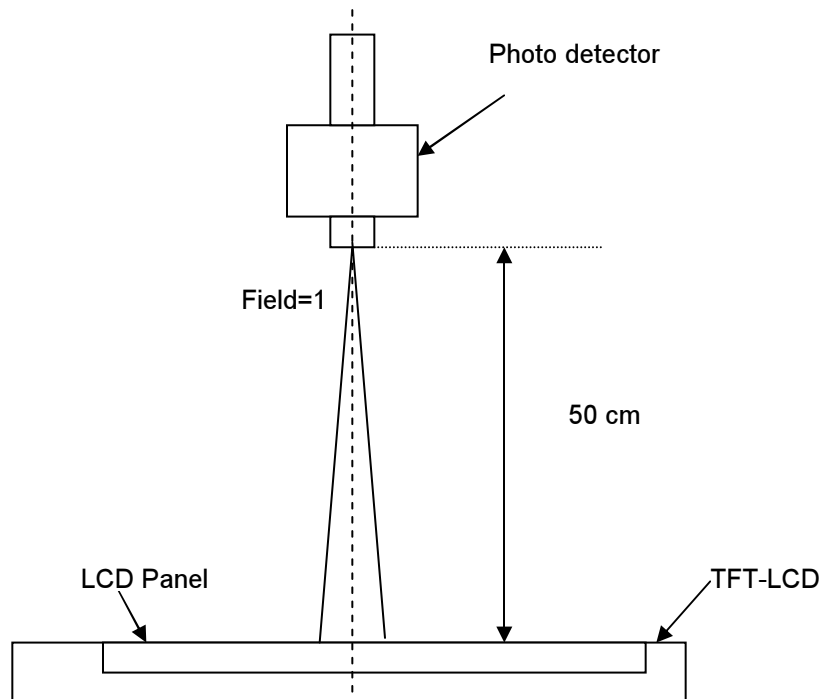
## Note 1: Definition of viewing angle

Viewing angle is the measurement of contrast ratio  $\geq 10$ , or  $\geq 5$ , at the screen center, over a  $180^\circ$  horizontal and  $180^\circ$  vertical range (off-normal viewing angles). The  $180^\circ$  viewing angle range is broken down as follows;  $90^\circ$  ( $\theta$ ) horizontal left and right and  $90^\circ$  ( $\Phi$ ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.



## Note 2: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.

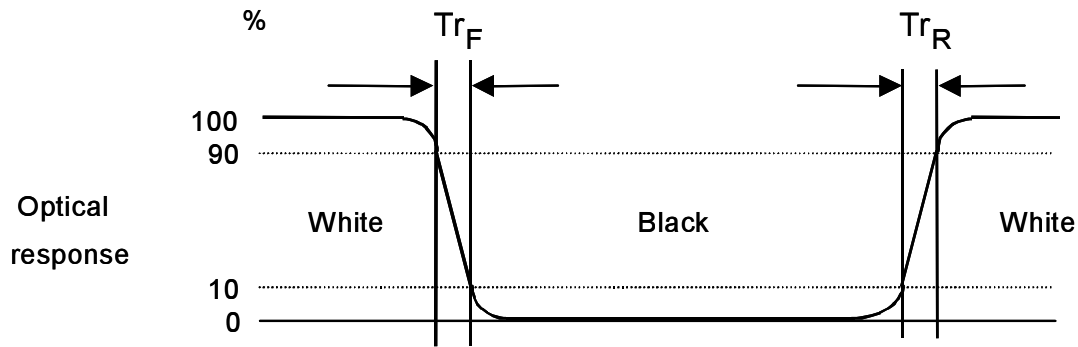


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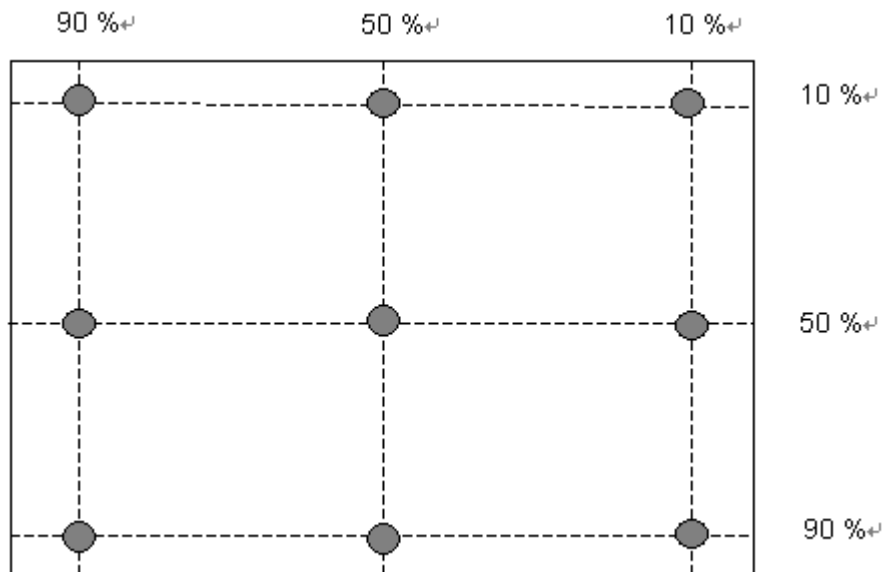
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Note 3: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time), and from "Full White" to "Full Black" (falling time), respectively. The response time is interval between the 10% and 90% of amplitudes. Please refer to the figure as below.



Note 4: 9 points position



Note 5:

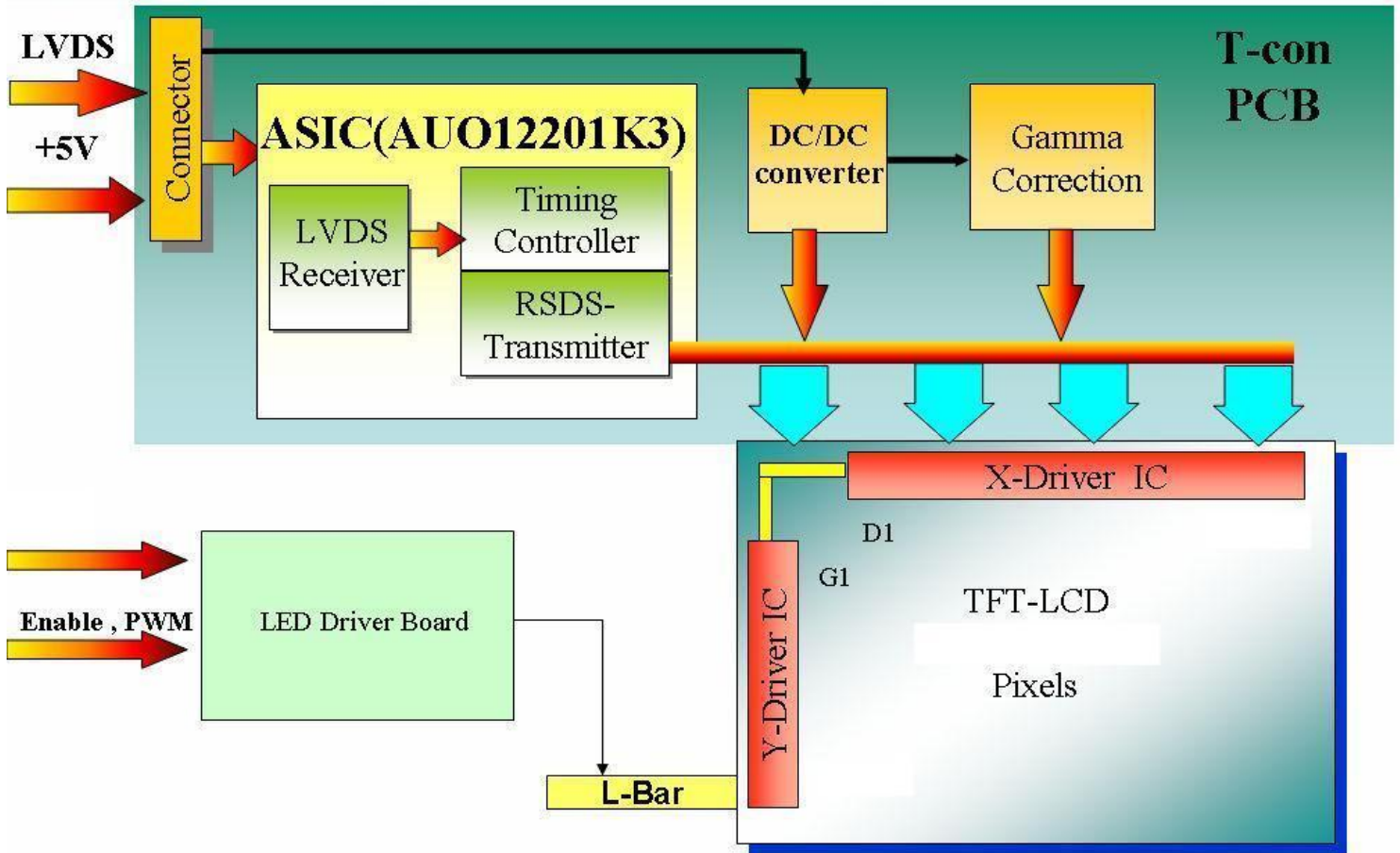
$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$$

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## 3. Functional Block Diagram

The following diagram shows the functional block of the Color TFT-LCD Module:



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## 4. Absolute Maximum Ratings

Absolute maximum ratings of the module are as following:

### 4.1 TFT LCD Module

| Item                    | Symbol | Min  | Max  | Unit   | Conditions |
|-------------------------|--------|------|------|--------|------------|
| Logic/LCD Drive Voltage | VDD    | -0.3 | +6.0 | [Volt] | Note 1,2   |

### 4.2 Absolute Ratings of Environment

| Item                  | Symbol | Min. | Max. | Unit  | Conditions |
|-----------------------|--------|------|------|-------|------------|
| Operating Temperature | TOP    | -0   | +50  | [°C]  | Note 3     |
| Operation Humidity    | HOP    | 5    | 95   | [%RH] | Note 3     |
| Storage Temperature   | TST    | -20  | +70  | [°C]  |            |
| Storage Humidity      | HST    | 5    | 95   | [%RH] |            |

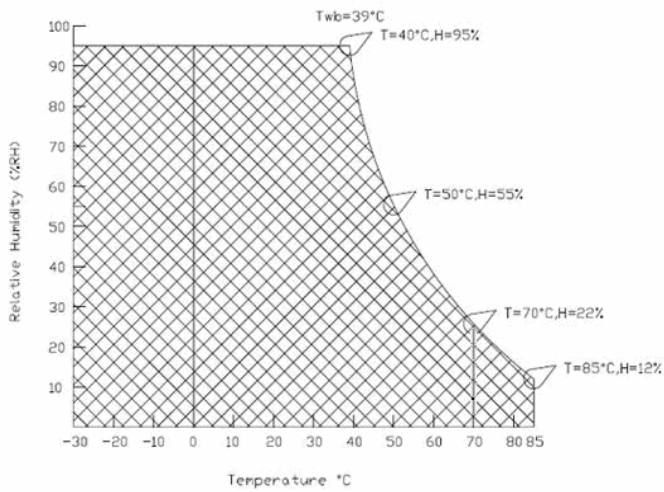
Note 1: With in Ta (25°C)

Note 2: Permanent damage to the device may occur if exceeding maximum values

Note 3: Temperature and relative humidity range are shown as the below figure.

1. 95% RH Max ( Ta  $\leq$  39°C)
2. Max wet-bulb temperature at 39°C or less. ( Ta  $\leq$  39°C)
3. No condensation

Note 4: Function Judged only





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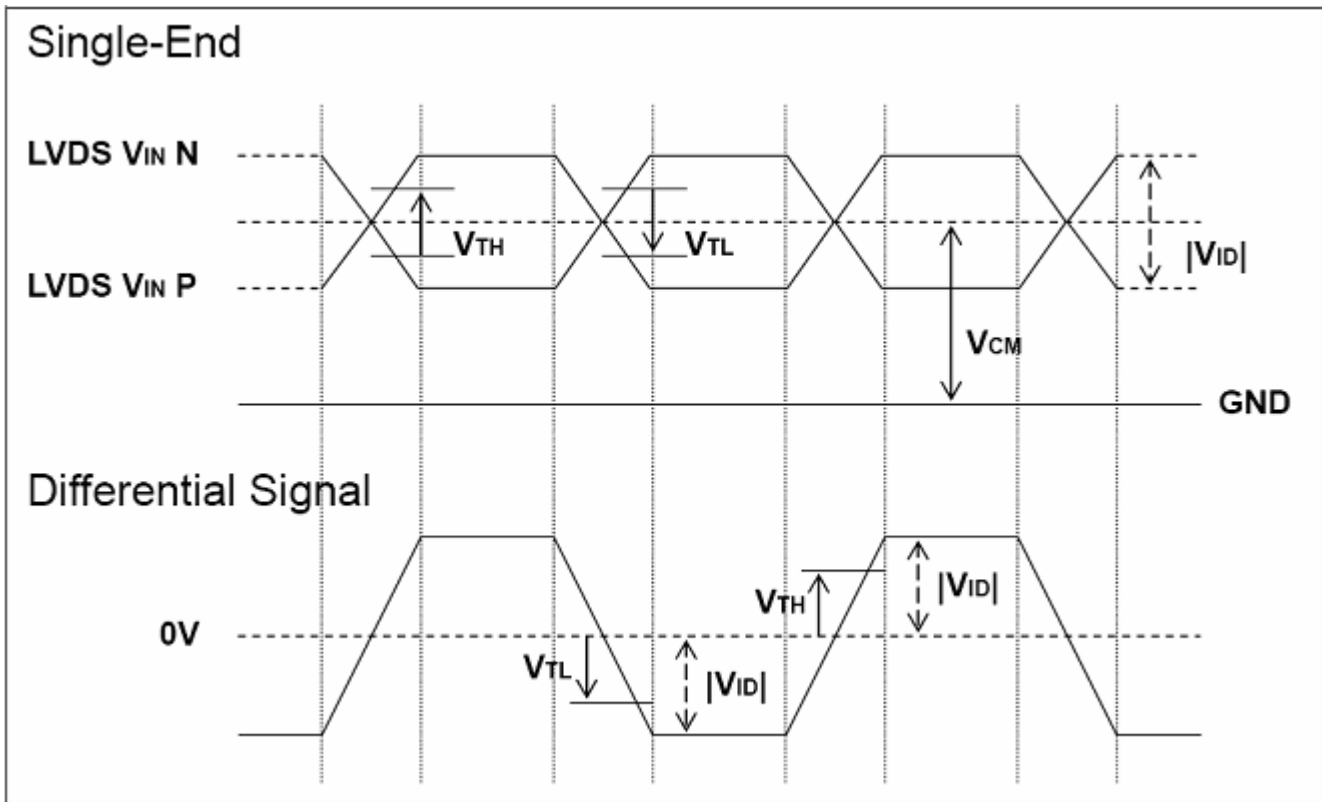
## 5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off. Please refer to specifications of SN75LVDS82DGG (Texas Instruments) in detail.

Each signal characteristics are as follows;

| Symbol     | Parameter                              | Min  | Typ  | Max  | Units | Condition                                      |
|------------|--|------|------|------|-------|--|
| $V_{TH}$   | Differential Input High Threshold      | -    | -    | +100 | [mV]  | $V_{CM} = 1.2V$<br><b>Note 1</b>               |
| $V_{TL}$   | Differential Input Low Threshold       | -100 | -    | -    | [mV]  | $V_{CM} = 1.2V$<br><b>Note 1</b>               |
| $ V_{ID} $ | Input Differential Voltage             | 100  | 400  | 600  | [mV]  | <b>Note 1</b>                                  |
| $V_{CM}$   | Differential Input Common Mode Voltage | +1.0 | +1.2 | +1.5 | [V]   | $V_{TH}-V_{TL} = 200mV$ (max)<br><b>Note 1</b> |

**Note1:** LVDS Signal Waveform



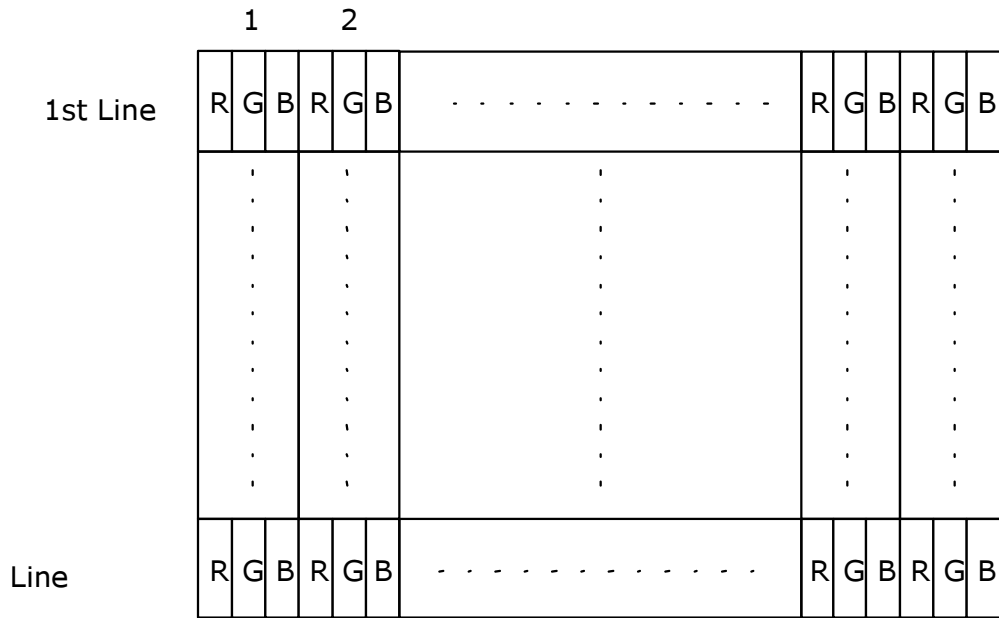
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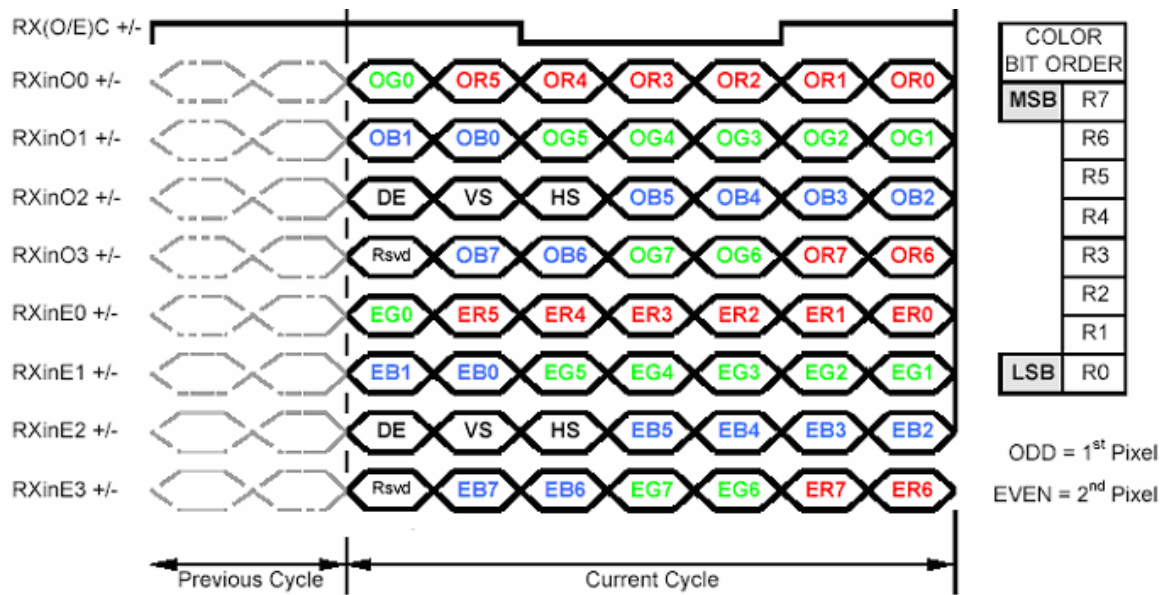
## 6. Signal Characteristic

### 6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.



### 6.2 The Input Data Format



Note1: DE only , VS, HS on EVEN channel are not used.

Note2: Please follow PSWG.

Note3: 8-bit in

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## 6.3 Signal Description

The module using one LVDS receiver SN75LVDS82(Texas Instruments). LVDS is a differential signal technology for LCD interface and high speed data transfer device. LVDS transmitters shall be SN75LVDS83(negative edge sampling). The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

| PIN # | SIGNAL NAME | DESCRIPTION  |
|-------|-------------|--|
| 1     | RxOIN0-     | Negative LVDS differential data input (Odd data)         |
| 2     | RxOIN0+     | Positive LVDS differential data input (Odd data)         |
| 3     | RxOIN1-     | Negative LVDS differential data input (Odd data)         |
| 4     | RxOIN1+     | Positive LVDS differential data input (Odd data)         |
| 5     | RxOIN2-     | Negative LVDS differential data input (Odd data, DSPTMG) |
| 6     | RxOIN2+     | Positive LVDS differential data input (Odd data, DSPTMG) |
| 7     | GND         | Power Ground   |
| 8     | RxOCLKIN-   | Negative LVDS differential clock input (Odd clock)       |
| 9     | RxOCLKIN+   | Positive LVDS differential clock input (Odd clock)       |
| 10    | RxOIN3-     | Negative LVDS differential data input (Odd data)         |
| 11    | RxOIN3+     | Positive LVDS differential data input (Odd data)         |
| 12    | RxEIN0-     | Negative LVDS differential data input (Even data)        |
| 13    | RxEIN0+     | Positive LVDS differential data input (Even data)        |
| 14    | GND         | Power Ground   |
| 15    | RxEIN1-     | Negative LVDS differential data input (Even data)        |
| 16    | RxEIN1+     | Positive LVDS differential data input (Even data)        |
| 17    | GND         | Power Ground   |
| 18    | RxEIN2-     | Negative LVDS differential data input (Even data)        |
| 19    | RxEIN2+     | Positive LVDS differential data input (Even data)        |
| 20    | RxECLKIN-   | Negative LVDS differential clock input (Even clock)      |
| 21    | RxECLKIN+   | Positive LVDS differential clock input (Even clock)      |
| 22    | RxEIN3-     | Negative LVDS differential data input (Even data)        |
| 23    | RxEIN3+     | Positive LVDS differential data input (Even data)        |
| 24    | GND         | Power Ground   |
| 25    | GND         | Power Ground   |
| 26    | GND         | Power Ground   |
| 27    | GND         | Power Ground   |
| 28    | POWER       | Power +5V  |
| 29    | POWER       | Power +5V  |
| 30    | POWER       | Power +5V  |

**Note: "Power Ground" stands for 0V.**

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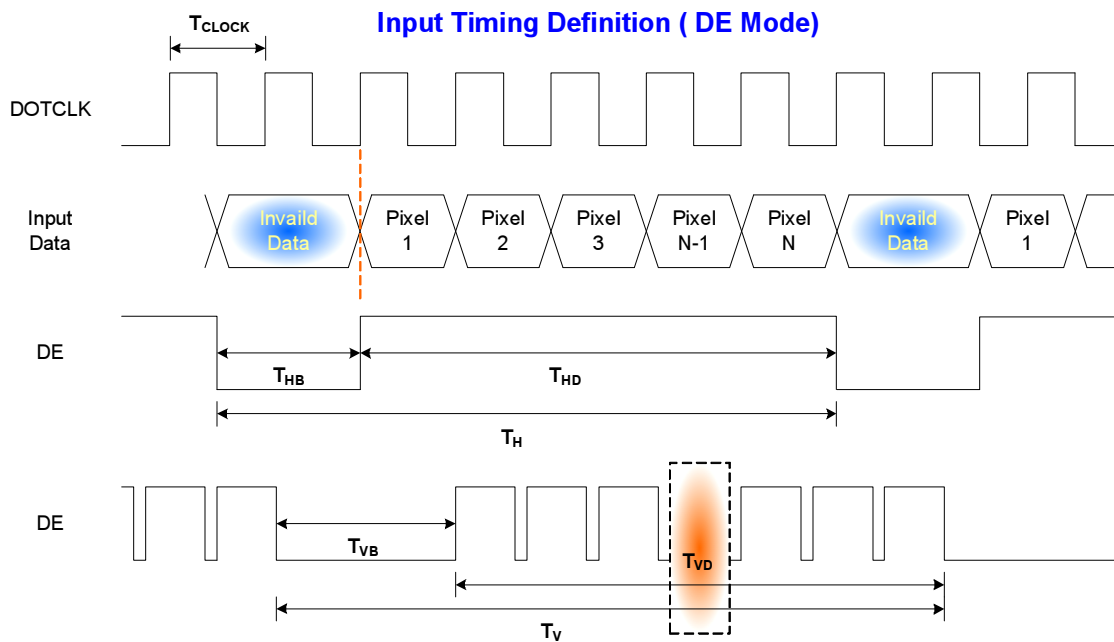
## 6.4 Interface Timing

### 6.4.1 Timing Characteristics

| Signal             | Item      | Symbol                        | Min  | Typ   | Max   | Unit |
|--------------------|-----------|-------------------------------|------|-------|-------|------|
| Vertical Section   | Period    | $T_v$                         | 1032 | 1066  | 1150  | Th   |
|                    | Active    | $T_{disp}(v)$                 | 1024 | 1024  | 1024  | Th   |
|                    | Blanking  | $T_{bp}(v)+T_{fp}(v)+PW_{vs}$ | 8    | 42    | 126   | Th   |
| Horizontal Section | Period    | $T_h$                         | 780  | 844   | 2047  | Tclk |
|                    | Active    | $T_{disp}(h)$                 | 640  | 640   | 640   | Tclk |
|                    | Blanking  | $T_{bp}(h)+T_{fp}(h)+PW_{hs}$ | 140  | 204   | -     | Tclk |
| Clock              | Period    | $T_{clk}$                     | 22.2 | 18.52 | 14.81 | ns   |
|                    | Frequency | Freq.                         | 44   | 54    | 67.5  | MHz  |
| Frame Rate         | Frequency | $1/T_v$                       | 49   | 60    | 75    | Hz   |

Note: DE mode only

### 6.4.2 Timing Diagram

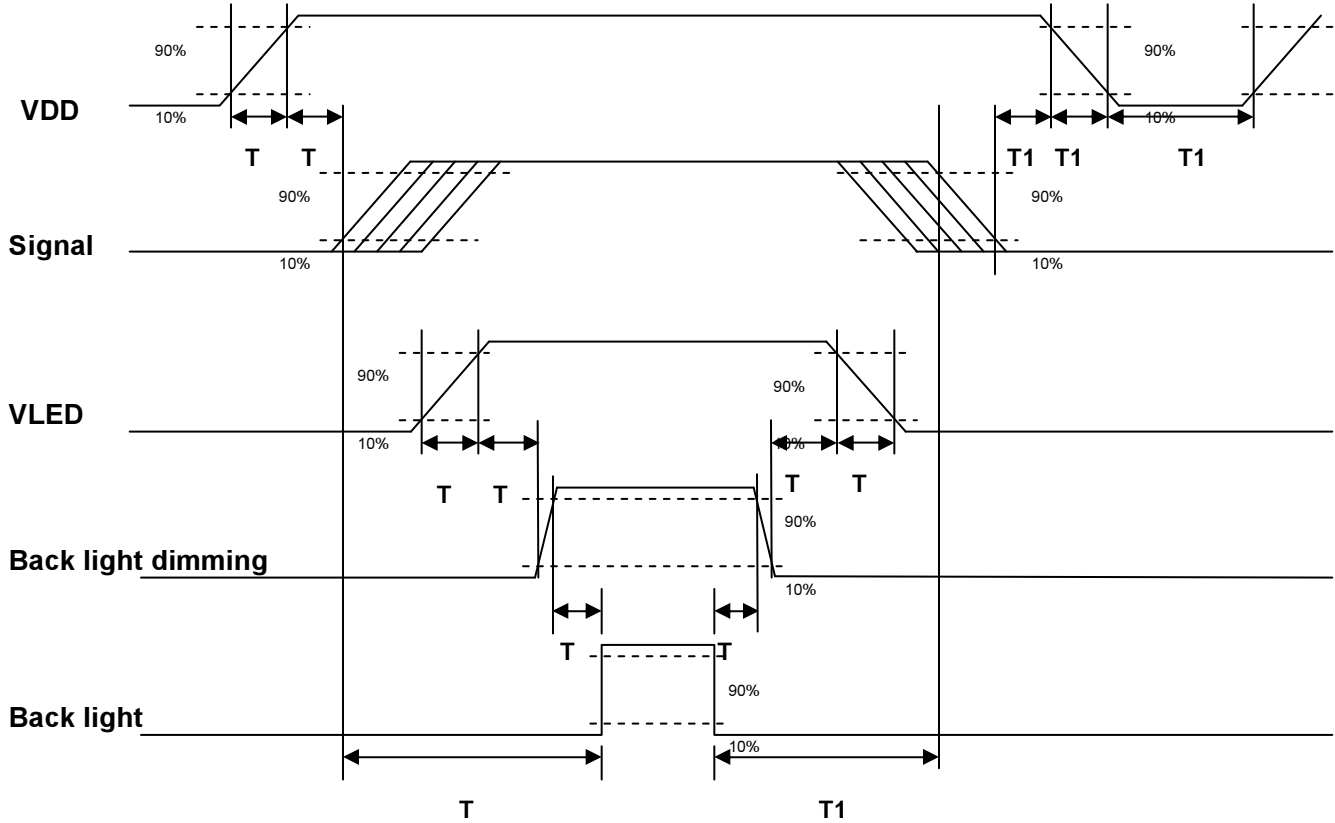


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## 6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence are as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



| Parameter | Value |      |      | Unit |
|-----------|-------|------|------|------|
|           | Min.  | Typ. | Max. |      |
| T1        | 0.5   | -    | 10   | [ms] |
| T2        | 30    | 40   | 50   | [ms] |
| T3        | 200   | -    | -    | [ms] |
| T4        | 0.5   | -    | 10   | [ms] |
| T5        | 10    | -    | -    | [ms] |
| T6        | 10    | -    | -    | [ms] |
| T7        | 0     | -    | -    | [ms] |
| T8        | 10    | -    | -    | [ms] |
| T9        | -     | -    | 10   | [ms] |
| T10       | 110   | -    | -    | [ms] |
| T11       | 0     | 16   | 50   | [ms] |
| T12       | -     | -    | 10   | [ms] |
| T13       | 1000  | -    | -    | [ms] |

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## 7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

### 7.1 TFT LCD Module

#### 7.1.1 Connector

|                              |                                      |
|------------------------------|--------------------------------------|
| Connector Name / Designation | Interface Connector / Interface card |
| Manufacturer                 | HRS                                  |
| Type Part Number             | DF14H-30P-1.25H                      |
| Mating Housing Part Number   | DF14H-30S-1.25C                      |

#### 7.1.2 Pin Assignment

| Pin# | Signal Name | Pin# | Signal Name |
|------|-------------|------|-------------|
| 1    | RxOIN0-     | 2    | RxOIN0+     |
| 3    | RxOIN1-     | 4    | RxOIN1+     |
| 5    | RxOIN2-     | 6    | RxOIN2+     |
| 7    | GND         | 8    | RxOCLKIN-   |
| 9    | RxOCLKIN+   | 10   | RxOIN3-     |
| 11   | RxOIN3+     | 12   | RxEIN0-     |
| 13   | RxEIN0+     | 14   | GND         |
| 15   | RxEIN1-     | 16   | RxEIN1+     |
| 17   | GND         | 18   | RxEIN2-     |
| 19   | RxEIN2+     | 20   | RxECLKIN-   |
| 21   | RxECLKIN+   | 22   | RxEIN3-     |
| 23   | RxEIN3+     | 24   | GND         |
| 25   | GND         | 26   | GND         |
| 27   | GND         | 28   | POWER       |
| 29   | POWER       | 30   | POWER       |

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## 8. Reliability Test

Environment test conditions are listed as following table.

| Items                             | Required Condition  | Note |
|-----------------------------------|---|------|
| Temperature Humidity Bias (THB)   | Ta= 50°C, 80%RH, 240hours   |      |
| High Temperature Operation (HTO)  | Ta= 50°C, 240hours  | 1    |
| Low Temperature Operation (LTO)   | Ta= 0°C, 240hours   | 1    |
| High Temperature Storage (HTS)    | Ta= 70°C, 240hours  | 1    |
| Low Temperature Storage (LTS)     | Ta= -20°C, 240hours   | 1    |
| Vibration Test<br>(Non-operation) | Acceleration: 1.5 G<br>Wave: Random<br>Frequency: 10 - 200 - 10 Hz<br>Sweep: 30 Minutes each Axis (X, Y, Z)         |      |
| Shock Test<br>(Non-operation)     | Acceleration: 50 G<br>Wave: Half-sine<br>Active Time: 20 ms<br>Direction: ±X, ±Y, ±Z (one time for each Axis) Axis) |      |
| Drop Test                         | Height: 61 cm, package test   |      |
| Thermal Shock Test (TST)          | -20°C/30min, 60°C/30min, 50 cycles  |      |
| ESD (Electro-Static Discharge)    | Contact Discharge: ± 8KV, 150pF(330Ω ) 1sec,<br>9 points, 25 times/ point.  | 2    |
|                                   | Air Discharge: ± 15KV, 150pF(330Ω ) 1sec,<br>9 points, 25 times/ point.   |      |

Note1: No function failure occurs.

Note2: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost Self-recoverable. No hardware failures.

Note3:

- Water condensation is not allowed for each test items.
- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.
- The reliability test is performed only to examine the TFT-LCD module capability.
- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.

